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Amendments to the Specification

Please replace the paragraph at page 3, lines 3-7 with the following amended paragraph:

- - Preferably, the synchronous semiconductor device further comprises a third data bus which is used to transmit data in the first bit configuration mode, the second bit configuration mode, and a third bit configuration mode and is arranged to be parallel with the first data bus and the second data bus, ~~wherein the third data bus is arranged crosswise to the first data bus and the second data bus~~ wherein every other second data bus is replaced with the third data bus.